

**SYSTEM AND METHOD FOR SETTING A CLOCK RATE OF A
MEMORY CARD**

Abstract of the Disclosure

A memory card that includes a buffer configured to receive transactions, a storage media, and a control circuit coupled to the buffer and the storage media is provided. The control circuit is configured to cause a clock signal to be provided to the buffer and the storage media at a clock rate that varies in dependence on a detected rate of the transactions received by the buffer.